

Compal Confidential

CIUSA/SB/SD

Cruze I/O Board Schematic Document

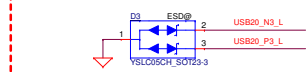
LS-E542P

2017-01-06

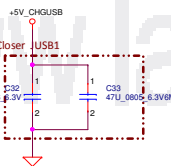
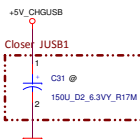
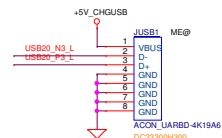
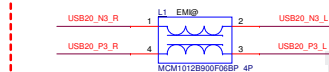
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		Sheet 1 of 3			

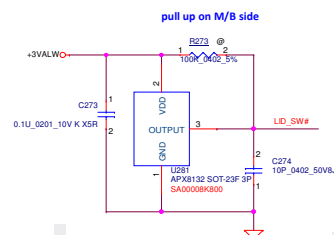
USB2.0 Port



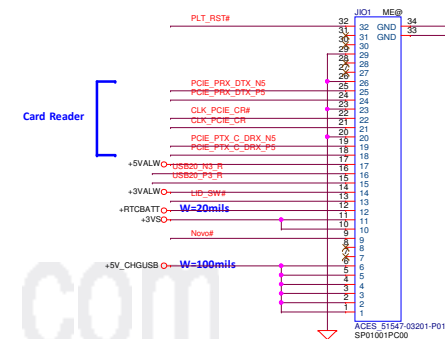
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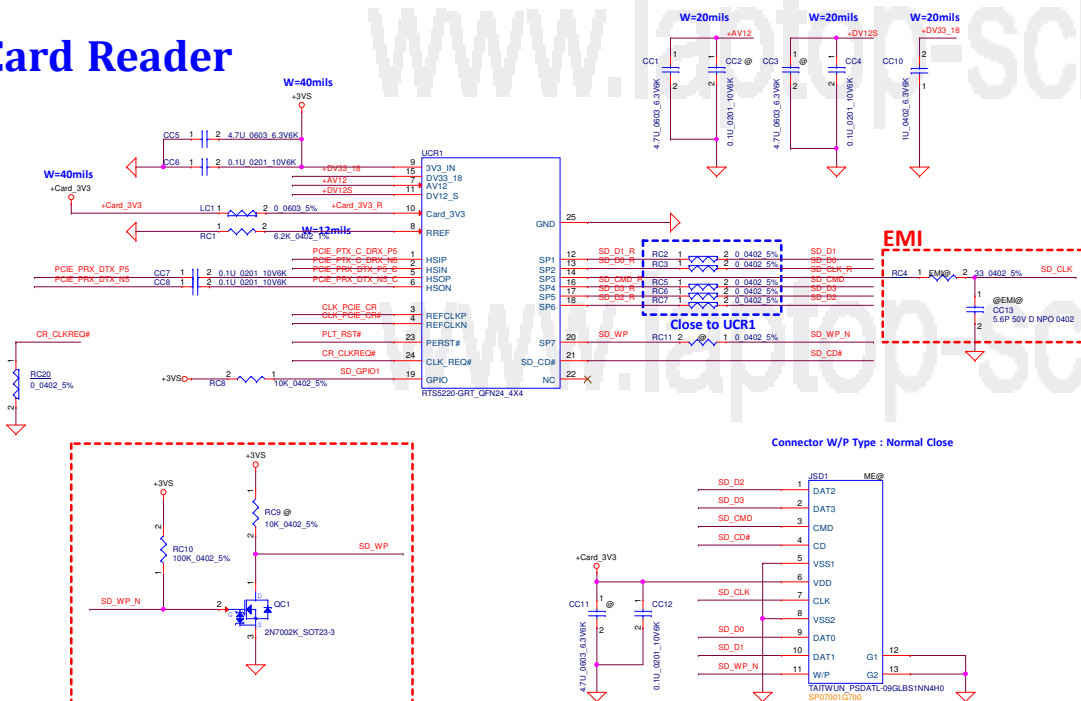
LID



I/O CONN

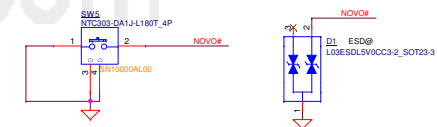


Card Reader

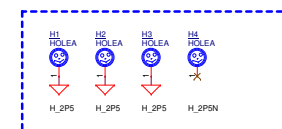
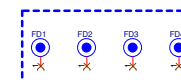
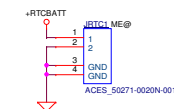


For W/P normal close type connector

Novo Button



RTC CONN



777

PCB 1YN LS-E542P REV0 IO/B

ZZZ X4E@

 SMT EMC FOR EE IO/B SE542 CIUSA
 X45OCY3RL01

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Version change list (P.I.R. List)

Page 1 of 1
for HW

Item	Reason for change	PG#	Modify List	Date	Phase
1	ME modify	2	Remove H5	09/15	EVT to DVT
2	DFX Request (Factory Rule)	2	Remove R101 and R102 (Remove 0-ohm path, remain choke)	11/09	DVT to PVT
3					
4					
5					
6					
7					
8					
9					
10					
11					
12					
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14					
15					

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